Mrs.Padmavati S. Pise
Associate Professor
padmavatipise@gmail.com

➢ **Educational Qualification: M.Tech. (Electronics Technology), B.E.(Electronics Engg.)**

➢ **Experience: 28 years**

➢ **Contact No. (optional) :**

➢ **Areas of Interest: VLSI Design , Embedded Systems**

➢ **Memberships: ISTE (LM-18341)**

➢ **Courses Handled: UG Level:**

1. VLSI Design
2. Network Analysis
3. Digital Systems & Microprocessor
4. Electronic System Design
5. Power Electronics
6. Power Electronics and Drives
7. System On Chip
8. Electronic Circuit Analysis & design-I
9. Electronic Measurement & Instrumentation
10. Microcontroller
11. Microprocessor

PG Level: 1. Mobile Computing

➢ **Administrative responsibilities:**

Institute Level:
- Presiding Officer of Internal Complaints Committee (Jan 2016 onwards)
- Coordinator of Robotics Club (June 2018 onwards)
- Time table Coordinator
- Member of Anti ragging Committee
- Member of Anti ragging Squad Committee
- Member of Examination Committee
- Member of Central Computing Facility Committee
- Admission Committee Member

Department Level:
- Timetable Coordinator
- Lab In-charge (Project Lab)
- Examination Coordinator
- Website Coordinator
- Gems ERP Coordinator
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➢ **Publications:**

International Journals and E-Letters: **3**
International Conferences: **4 (1 Best paper award)**

National Conferences: **2**

➢ **Patents: Nil**

➢ **Research Projects :**

➢ **Awards and Accolades:**
    1. Best Paper Award (ICETETA 2017)