

Course Name: Structured Computer Organization and Microprocessors

Course Code: 201DSL203

Day and Date: Friday, 20/01/2023

Seat No:

Time: 10:00 am to 12:00 pm

Max. Marks- 50

Instructions:

- i. Question No. 1 & 2 is compulsory.
- ii. Figure to the right indicate full marks.

BT	CO's	Q.No.		Marks
		Q.1	All Questions are compulsory	20
	CO2	a	Explain the invention of Vacuum tubes in first generation.	6M
1,2	CO1	b	Explain Carry look ahead adder in detail.	7M
1,2	CO1	c	What is an Instruction cycle?	7M
		Q.2	All Questions are compulsory	10
1,2	CO2	a	Write a short note on 8085 Bus Structure	4M
1,2	CO1	b	Explain Instruction fetch operation with an example	6M
		Q.3	All Questions are compulsory	10
1,2	CO1	a	Explain Pentium Pro Architecture in brief.	3M
1,2	CO1	b	State the difference between Intel Core I3, I5 and I7	7M
		Q.4	Attempt any two out of three questions	10
1,2	CO1	a	What are the properties of a Coprocessor?	5M
1,2	CO1	b	State the difference between Single-chip and Multi Microprocessors.	5M
		c	What are the types of the Internal ROM?	5M
