

D.Y. Patil College of Engineering & Technology, Kolhapur
Department of Electronics & Telecommunication Engineering

Year : 2023-24 S Y B.Tech. Time-Table

Semester-II Classroom-306

DAY	9.00 to 10.00	10.00 to 11.00	11.15 to 12.15	12.15 to 1.15	2.00 to 3.00	3.00 to 4.00
MON	ECAD-II LAB S1		ECAD-II	DSDV	SS	ICS
	DSDV LAB S2					
	LIC LAB S3					
TUE	MINIPROJECT LAB S1		DSDV	ICS	SHIKUYA TRAINING	FM
	ECAD-II LAB S2					
	DSDV LAB S3					
WED	SHIKUYA TRAINING		LIC LAB S1		LIC	SS
			MINIPROJECT LAB S2			
			ECAD-II LAB S3			
THU	DSDV LAB S1		ECAD-II	DSDV	FM	LIC
	LIC LAB S2					
	MINIPROJECT LAB S3					
FRI	SS TUT S1, S3	SS TUT S2	LIC	SS	ECAD-II	ICS
	ICS TUT S2	ICS TUT S3				
SAT	ICS TUT S1	ICS TUT S1	CLUB ACTIVITIES			

Theory ,Practical & Tutorial		Abbreviations	Faculty	Pract./TUT. Venu
1	Electronics Circuit Analysis & Design - II	ECAD-II Lab.	Dr. T.B.Mohite-Patil Ms.P.P.Farakate (S1,S2,S3)	Lab-5
2	Mini Project Lab.	Mini Project Lab.	Mrs. R.N.Gaikwad (S1) Dr.M.V.Bhanuse (S2,S3)	Lab-3,2
3	Digital System Design using Verilog	DSDV	Prof.Mrs.P.S.Pise	Lab-1
4	Signal system	SS	Dr. T.B.Mohite-Patil (S1, S2) Ms.P.P.Farakate (TUT.) (S3)	Lab-2 Lab-4
5	Linear integratade circuit	LIC	Prof.S.R.Khot	Lab-7
6	Instrumentation &Control system	ICS	Prof.Mrs.N.S.Vatkar	Lab-4
7	Financial Management	FM	Prof.Mrs.N.S.Vatkar	

Time table
Co-Ordinator

Head of the Department

Dean Academics

Principal

W.e.f - 29-01-2024