

No Preview
Available

Total No. of Question : [4]

Registration No. :

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Programme Name : F.Y.B.Tech
Regular F.Y.B.Tech. ESE Sem. I (A.Y.2023-24) Dec.2023
I SEMESTER (2023 BATCH)
231FYL113-Elements of Eletrical and Eletronics Engineering

Duration : [- - -]

Date : -

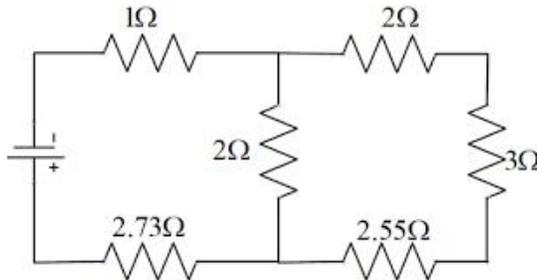
Day : -

Marks : 50

Instructions :

(Q1) Attempt the following [20.0]

(1.1) Find the total Resistance [6.0]



(1.2) The equation of alternating voltage is given by $V = 333.22 \sin 628t$. Find (i) RMS value [7.0]
(ii) Frequency (iii) Average value.

(1.3) Explain Construction & operating principle of transformer [7.0]

(Q2) Attempt the following [10.0]

(2.1) Explain transistor types with the symbol [4.0]

(2.2) With the help of neat circuit diagram and waveform explain the operation of full wave Centre tapped rectifier circuit. [6.0]

OR [2.2 / 2.3]

(2.3) Explain features of IC voltage regulator 78XX [6.0]

(Q3) Attempt the following [10.0]

(3.1) Draw truth table & Symbol of AND, NOR & EX-OR gate [3.0]

(3.2) Explain why NAND & NOR called as universal gates [7.0]

OR [3.2 / 3.3]

(3.3) Reduce the Boolean expression: $F = AB + (AC)' + AB'C(AB + C)$. [7.0]

(Q4) Attempt any two out of three questions [10.0]

(4.1) What is Transducer? Explain classification of Transducers [5.0]

(4.2) Explain working of temperature transducer. [5.0]

(4.3) Explain working of LVDT: displacement transducer with block diagram. [5.0]
