

D. Y. Patil College of Engineering and Technology Kolhapur
Department of Electronics & Telecommunication Engineering

SEMESTER III												
Course Code	Course Type	Course Name	Teaching Scheme				Theory			Practical		Total Marks
			Credits	L	P	T	ISE	MSE	ESE	INT	OE/PoE	
231ETPCCL201	PCC	Electronics Circuit Analysis and Design – I	3	3	-	-	20	30	50	-	-	100
231ETPCCL202		Analog and Digital Communication	3	3	-	-	20	30	50	-	-	100
231ETPCCL203		Network Analysis	2	2	-	-	-	-	50	-	-	50
231ETPCCP201		Electronics Circuit Analysis and Design Lab	1	-	2	-	-	-	-	25	25	50
231ETPCCP202		Analog and Digital Communication Lab	1	-	2	-	-	-	-	25	25	50
231ETPCEP201	CEP/FP	Society based Mini-Project	2	-	4	-	50	-	-	-	-	50
231ETMDML201	MDM-1	Digital Electronics	2	2	-	-	20	-	30	-	-	50
231ETVECL201	VEC	Personal Values and Ethics	2	2	-	-	20	30	-	-	-	50
231ETOECL201	OEC-1	Sensors & Actuator (ODL Only)	4	2	-	-	20	30	50	25	-	125
231ETOECL202		Basics of Arduino (ODL Only)		2								
231ETHSSML201	Entrepreneurship/ Economics/ Management course	Financial Management	1	1	-	-	25	-	-	-	-	25
231ETHSSMP201		Financial Management Lab.	1	-	2	-	-	-	-	25	-	25
231ETMCL201	MC	Finishing School Training III	Audit	3*	-	-	50	-	-	-	-	Grade
231ETCCAL201	CCA	Liberal Learning-I (Garuda)	Audit	2 [#]	-	-	50	-	-	-	-	Grade
231ETCCAL202		Liberal Learning-II (Robotics)		2 [#]								
231ETCCAL203		Liberal Learning-III (IoT)		2 [#]								
		Total	22	17	10		275	120	230	100	50	675

* - Values not included in total, # - 2 contact hrs per club

SEMESTER IV												
Course Code	Course Type	Course Name	Teaching Scheme				Theory			Practical		Total Marks
			Credits	L	P	T	ISE	MSE	ESE	INT	OE/PoE	
231ETPCCL204	PCC	Electronics Circuits Analysis & Design – II	3	3	-	-	20	30	50	-	-	100
231ETPCCL205		Digital System Design using Verilog	3	3	-	-	20	30	50	-	-	100
231ETPCCL206		Instrumentation & Control Systems	2	2	-	-	-	-	50	-	-	50
231ETPCCP204		Electronics Circuits Analysis & Design Lab. II	1		2	-	-	-	-	25	25	50
231ETPCCP205		Digital System Design using Verilog Lab.	1		2	-	-	-	-	25	25	50
231ETMDML202	MDM-2	Microcontrollers(CISC)	2	2	-	-	20	-	30	-	-	50
231ETVECL202	VEC (Environmental Study)	Environmental Study	2	2	-	-	-	-	50	-	-	50
231ETHSSML202	Entrepreneurship/Economics/Management course	Industrial Management & Startups	2	2	-	-	25	-	-	25	-	50
231ETAECP201	AEC	Electronics Workshop Practice	2	1	2	-	-	-	-	25	25	50
231ETOECL203	OEC-II	Electronic Instrumentation	2	2	-	-	-	-	50	-	-	50
231ETOECL204		Electronic Automation		2								
231ETVSECL201	VSEC	Model Based Programming & Simulation	1	1		-	25	-	-		-	25
231ETVSECP201	VSEC	Model Based Programming & Simulation Lab.	1		2	-		-	-	25	-	25
231ETMCL202	MC	Finishing School Training IV	Audit	2*	-	-	50	-	-	-	-	Grade
231ETCCAL204	CCA	Liberal Learning-I (Garuda)	Audit	2 [#]	-	-	50	-	-	-	-	Grade
231ETCCAL205		Liberal Learning-II (Robotics)		2 [#]								
231ETCCAL206		Liberal Learning-III (IoT)		2 [#]								
		Total	22	20	8	-	160	60	330	125	75	650
231ETHONL201	HC (Optional)	Honors Paper- I Electronics equipment Integration and prototype building	04	3	2	-	20	30	50	-	25	125

* - Values not included in total, # - 2 contact hrs per club

SEMESTER V												
Course Code	Course Type	Course Name	Teaching Scheme				Theory			Practical		Total Marks
			Credits	L	P	T	ISE	MSE	ESE	INT	OE/PoE	
231ETPCCL301	PCC	Linear Integrated circuit	3	3	-	-	20	30	50	-	-	100
231ETPCCL302		Microprocessor and Microcontroller	3	3	-	-	20	30	50	-	-	100
231ETPCCL303		Cellular & Mobile Communication	3	3	-	-	20	30	50	-	-	100
231ETPCCL301		Linear Integrated circuit Lab	1	-	2	-	-	-	-	25	25	50
231ETPCCP302		Microprocessor and Microcontroller Lab	1	-	2	-	-	-	-	25	25	50
231ETPCCP303		Cellular & Mobile Communication Lab	1	-	2	-	-	-	-	25	-	25
231ETMDML301	MDM-3	Microcontrollers(RISC)	3	3	-	-	20	30	50	-	-	100
231ETMDMP301		Microcontrollers(RISC)-Lab	1	-	2	-	-	-	-	25	-	25
231ETOECL301	OCE-III	Biomedical Instrumentation	2	2	-	-	-	-	50	-	-	50
		Electronics Automation		2								
231ETPECL301	PEC 1	Fiber Optic Communication	4	4	-	-	20	30	50	25	-	125
231ETPECL302		Fundamentals of semiconductor devices		4								
231ETPECL303		Information Theory & Coding		4								
231ETMCL301	MC	Finishing School Training V	Audit	3*	-	-	50	-	-	-	-	Grade
231ETCCAL301	CCA	Liberal Learning-I (Garuda)	Audit	2 [#]	-	-	50	-	-	-	-	Grade
231ETCCAL302		Liberal Learning-II (Robotics)		2 [#]								
231ETCCAL303		Liberal Learning-III (IoT)		2 [#]								
231ETCCAL304		Liberal Learning-I (PSoC)		2 [#]								
		Total	22	30	8	-	200	150	300	125	50	725
231ETHONL301	HC (Optional)	Honors Paper- II (ODL) Semiconductor device modelling and simulation	04	2	-	-	20	30	50	25	-	125

* - Values not included in total, # - 2 contact hrs per club

SEMESTER VI												
Course Code	Course Type	Course Name	Teaching Scheme				Theory			Practical		Total Marks
			Credits	L	P	T	ISE	MSE	ESE	INT	OE/PoE	
231ETPCCL304	PCC	Embedded Systems	3	3	-	-	20	30	50	-	-	100
231ETPCCL305		Fundamentals of Digital Signal Processing	3	3	-	-	20	30	50	-	-	100
231ETPCCL306		Electromagnetic Engineering	2	2	-	-	-	-	-	-	-	50
231ETPCCP304		Embedded Systems Lab.	1		2	-	-	-	-	25	25	50
231ETPCCP305		Fundamentals of Digital Signal Processing Lab.	1		2	-	-	-	-	25	25	50
231ETMDML303	MDM-4	Control System	2	2	-	-	20	-	30	-	-	50
231ETPECL304	PEC-2	Antenna & wave propagation	4	4								125
231ETPECL305		CMOS VLSI design		4	-	-	20	30	50	25	-	
231ETPECL306		Wireless Sensor Network		4								
231ETPECL307	PEC-3	Microwave Theory	3	3								100
231ETPECL308		ASIC Design		3	-	-	20	30	50		-	
231ETPECL309		Satellite Communication		3								
231ETPECP307	PEC-3	Microwave Theory Lab.	1		2							25
231ETPECP308	ASIC Design Lab.			2	-				25	-		
231ETPECP309	Satellite Communication Lab.			2								
231ETVSECL301	VSEC	Data Structures & Algorithms using C++	2	1	2	-	25	-	-	-	-	50
231ETMCL302	MC	Finishing School Training VI	Audit	3*	-	-	50	-	-	-	-	Grade
231ETCCAL305	CCA	Liberal Learning-I (Garuda)	Audit	2 [#]								Grade
231ETCCAL306		Liberal Learning-II (Robotics)		2 [#]								
231ETCCAL307		Liberal Learning-III (IoT)		2 [#]	-	-	50	-		-	-	
231ETCCAL308		Liberal Learning-IV (PSoC)		2 [#]								
		Total	22	34	8	-	225	120	230	100	50	700
231ETHONL302	HC (Optional)	Honors Paper- III Digital IC Design	04	2	2	-	20	30	50	25	-	125

* - Values not included in total, # - 2 contact hrs per club

SEMESTER VII												
Course Code	Course Type	Course Name	Teaching Scheme				Theory			Practical		Total Marks
			Credits	L	P	T	ISE	MSE	ESE	INT	OE/PoE	
231ETPCCL401	PCC	Data Communication and Networks	4	3		-	20	30	50	-	-	100
231ETPCCL402		Advanced Mobile Communication	2	2	-	-	-	-	50	-	-	50
231ETPCCP401		Data Communication and Networks-Lab			2		-	-	-	25	25	50
231ETMDML401	MDM-5	Mechatronics	2	2	-	-	20	-	30	-	-	50
231ETRML401	RM	Research Methodology (ODL)	4	2	-	-	20	30	50	25	-	125
231ETPECL401	PEC-4	Digital Image Processing	3	3	-	-	20	30	50	-	-	100
231ETPECL402		System on Chip		3								
231ETPECL403		Power Electronics		3								
231ETPECL404	PEC-5	Image Processing & Analysis	3	3	-	-	20	30	50	-	-	100
231ETPECL405		Cyber Security		3								
231ETPECL406		Software Networks		3								
231ETPRP401	PR	Project	4	-	4	-	-	-	-	100	25	125
231ETCCAL401	CCA	Liberal Learning-I (Garuda)	Audit	2 [#]	-	-	50	-	-	-	-	Grade
231ETCCAL402		Liberal Learning-II (Robotics)		2 [#]								
231ETCCAL403		Liberal Learning-III (IoT)		2 [#]								
231ETCCAL404		Liberal Learning-IV (PSoC)		2 [#]								
		Total	22	27	6	0	150	120	280	150	50	700
231ETHONL401	HC (Optional)	Honors Paper- IV - MEMS Technology	04	3	1	-	20	30	50	25	-	125

- 2 contact hrs per club

SEMESTER VIII												
Course Code	Course Type	Course Name	Teaching Scheme			Theory			Practical		Total Marks	
			Credits	L	P	T	ISE	MSE	ESE	INT		OE/PoE
231ETPCCL403	PCC	Programming in Java (ODL)	4	1		-	20	30	50	25		125
231ETPECL407	PEC-6	Basics of Machine learning (ODL)	2	1	-	-	-	-	50	-	-	50
231ETPECL408		Internet of Things (ODL)		1								
231ETPECL409		Introduction to Artificial Intelligence (ODL)		1								
231ETINTP401	INT	Internship	12	-	8	-	-	-	-	200	200	400
231ETMDMP401	MDM-6	Industrial Automation and Control (ODL)	2	2		-	-	-	-	50	-	50
		Total	20	5	8	-	20	30	100	275	200	625
231ETHOLN402	HC (Optional)	ODL- Honors Paper- V Design and Analysis of VLSI Subsystems (ODL)	02	1	-	-	-	20	30	-	-	50



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