Seat No.	

SL-463

Total No. of Pages : 2

S.E. (CSE) (Part - II) (Semester - IV) (Theory) Examination, April - 2017
COMPUTER ORGANIZATION
Sub. Code : 63533

Day and Date : Saturday, 29 - 04 - 2017
Time : 9.00 a.m. to 11.00 a.m.

Total Marks : 50

Instructions :
1) **Solve all questions.**
2) **Q.No. 1 and Q.No. 4 is compulsory.**
3) **Solve any one from Q.No. 2, Q.No.3 and one from Q.No. 5, Q. No. 6.**
4) **Figures to right indicate full marks.**

Q1) a) Write down all decimal codes equivalent to decimal digits 0 to 9. [6]

b) Illustrate floating point addition algorithm for [7]

X = 0 01111111 100000000000000.....00000 (32 Bit)

Y = 0 10000111 001010110100000.....00000 (32 Bit)

Q2) a) Compare the First, Second and Third generation of computer. [6]

b) Explain AMD Processor. [6]

Q3) a) Explain overview of CPU behavior. What are user and supervisor modes? [6]

b) Explain Error detection and correction logic. [6]

Q4) a) Write HDL description of the multiplier for 8-bit two's complement fractions. [6]

b) Explain GCD Processor with example. [7]

P.T.O.

Q5) a) Explain Wilkes basic structure of a micro programmed control unit. [6]

b) Explain control field formats. [6]

Q6) a) Explain memory types. [6]

b) Explain structure of an associative memory. [6]

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