Seat No.	

SF-223

Total No. of Pages : 2

S.E. (CSE) (Part -II) (Semester - IV) Examination, November - 2017
COMPUTER ORGANIZATION (Theory)
Sub. Code : 63533

Day and Date : Friday, 03 - 11 - 2017 **Total Marks : 50**
Time : 9.30 a.m. to 11.30 a.m.

Instructions :
1) **Solve all Questions**
2) **Q.No.1 and Q.No.4. is compulsory**
3) **Solve any one from Q.No.2,Q.No. 3 and one from Q.no.5,Q.No.6**
4) **Figures to right indicate full marks.**

SECTION - I

Q1) a) Write the IEEE 754 64 bit floating point number format. **[1]**

b) Write Non-restoring division algorithm for unsigned integers. **[6]**

c) Illustrate Booth multiplication algorithm for **[6]**

X=10110011 Y=11010101

Q2) a) Write a short note on1. Electronic Era 2. Mechanical Era. **[6]**

b) Write a short note on 1.RISC Computer 2.CISC Computer. **[6]**

Q3) a) Explain P-IV Processor. **[6]**

b) Explain overview of CPU Behavior. **[6]**

Q4) a) Explain Memory allocation. **[7]**

b) Explain Cache Organizations. **[6]**

P.T.O.

Q5) a) Design GCD processor using classical method. **[6]**

b) Draw and explain flowchart of the accumulator based CPU **[6]**

Q6) a) Design two's complement multiplier control unit using micro programmed approach **[6]**

b) Write a short note on

i) Parallelisms in microinstructions.

ii) Microoperation timing. **[6]**

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