



D.Y. Patil College of Engineering & Technology, Kolhapur
Department of Electronics & Telecommunication Engineering

TY (E&TC) Semester : -I

Academic Year: 2020-21

Class Room: PG Classroom

Classcoordinator- Prof. Mrs. M.V. Bhanuse

With effect from: 14/9/2020

| DAY/ TIME | 09.00- 10.00 | 10.00- 11.00 | 11:00 to 11:15 | 11.15- 12.15 | 12.15- 01.15 | 1:15 to 2:00 | 02.00- 03.00 | 03.00- 04.00 |
|--------------|---|-----------------|-------------------|---|-----------------|------------------------|---|-----------------|
| MON | OC (TBMP) | SS (/SVS) | Recess | EME (/MVB) | DVLSI (PSP) | Lunch Break | DVLSI Practicals | |
| TUE | DVLSI (PSP) | SS (/SVS) | | OC (TBMP) | SM (AMK) | | OC Practicals | |
| WED | SS (/SVS) | DVLSI (PSP) | | EME (/MVB) | | | SM Practicals | |
| THU | OC (TBMP) | DVLSI (PSP) | | EME (/MVB) | | | | |
| FRI | Co-curricular/extra- curricular Activity | | | Co-curricular/extra- curricular Activity | | | Co-curricular/extra- curricular Activity | |
| SAT | Co-curricular/extra- curricular Activity | | | Co-curricular/extra- curricular Activity | | | Co-curricular/extra- curricular Activity | |

| Subject | Abbrevitions | Faculty Names |
|---------------------------------|--------------|----------------------------|
| Open Elective-I(OELE-I) | SDC | Dr. S.D. Chede |
| Signals & Systems (SS) | /SVS | Prof.Dr. S.V. Sankpal |
| Digital and VLSI Design (DVLSI) | /PSP | Prof. P. S. Pise |
| Opticalal Communication (OC) | TBMP | Prof.Dr. T.B. Mohite Patil |
| Electromagnetic Engg.(EME) | /MVB | Prof. Mrs. M.V. Bhanuse |
| Simulation and modeling (SM) | AMK | Mr.A.M.Kasabe |

Time table

Co-Ordinator

HoD

Dean Academics

Principal