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**D.Y. PATIL COLLEGE OF ENGINEERING & TECHNOLOGY**

**22SYAIML203303**

**Q. Paper Code:**

**KASABA BAWADA KOLHAPUR-416006**

**(An Autonomous Institute)**

**S. Y. B. Tech-** AIML**(Semester-III)**

**END SEMESTER EXAMINATION, OCT/NOV- 2021-22**

COURSE NAME: Computer Architecture and Microprocessors, COURSE CODE:201AIMLL203

Seat No :

**Day and Date: Tuesday, 08/02/2022**

**Time: 11.00am to12.30pm Max. Marks- 50**

***Instructions:***

1. *All Questions are compulsory.*
2. *Figure to the right indicate full marks.*
3. *Give suitable general Instructions*
4. *Any other Course Specific Instructions.*

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| **BTL** | **CO** | **Q. No.** |  | **Marks** |
|  |  | **Q.1)** | **Attempt the following** | **20** |
|  | **CO1** | **a** | Explain evolution of intel X86 architecture | **7** |
|  | **CO2** | **b** | Explain Pentium 4 and Power PC Cache Organization | **7** |
|  | **CO3** | **c** | Explain DMA concept in detail | **6** |
|  |  | **Q.2)** | **Attempt the following** |  |
|  | CO4  CO5 | a | I) Explain difference between Microprocessor & Microcontroller OR  II)Internal structure of Pentium pro | **7** |
|  | CO4 | b | I)Explain data transfer group of instructions in 8085with example  OR  II)Explain instruction execution in 8085 | **8** |
|  |  | **Q.3)** | **Attempt the following** |  |
|  | CO5  CO4 | a) | I)Explain memory management in Pentium processor  OR  II)Explain 8051 microcontroller architecture | **7** |
|  | CO4  CO5 | b) | I) Explain addressing modes in 8085 OR  II) Explain special Pentium registers | **8** |

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